

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
ATTY. DOCKET NO. 13688

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In re Patent Application of Richard S. NORMAN et al.

Serial No. 09/870,766

Group Art Unit: 2661

Filed: June 1, 2001

Examiner:

For: **CELL-BASED SWITCH FABRIC ARCHITECTURE IMPLEMENTED
ON A SINGLE CHIP**

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Supplemental to the Information Disclosure Statement filed on August 28, 2001, May 13, 2002 and August 14, 2003 this Information Disclosure Statement is being filed to satisfy the duty under 37 CFR 1.56 to disclose to the Office information, known to individuals associated with the filing and prosecution of the subject application, which is material to the examination of the application.

In accordance with 37 CFR 1.97(g) and (h), this statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b).

This information disclosure statement is being filed before the mailing date of a first official action on the merits and therefore applicant respectfully requests consideration under 37 CFR 1.97(b).

In compliance with 37 CFR 1.98(a)(1), a list of all patents, publications or other information submitted for consideration by the Office is hereby provided by way of the attached Form PTO 1449.

In compliance with 37 CFR 1.98(a)(2), also enclosed is a legible copy of:

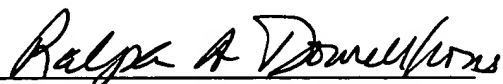
- i) each United States and foreign patent;
- ii) each publication or that portion which caused it to be listed; and
- iii) all other information or that portion which caused it to be listed, excluding any copies of a United States patent application.

It is respectfully requested that the information be expressly considered by the Examiner and that the references be made of record and appear among the "References Cited" on any patent to issue therefrom.

The Patent Office is hereby authorized to charge any deficiency, or credit any overpayment in fees to Deposit Account Number 04-1577.

Respectfully submitted,

DATE: Oct 23, 2003


Ralph A. Dowell, Reg. No. 26,868
DOWELL & DOWELL, P.C.
1215 Jefferson Davis Highway
Suite 309
Arlington, VA 22202-3124, U.S.A.
Telephone: (703) 415-2555
Fax: (703) 415-2559

Encls.: Form PTO-1449
All references listed on Form PTO-1449
Acknowledgement Card

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)
13688

Application Number
09/870,766

Applicant(s)
Richard S. NORMAN et al.

Filing Date
June 1, 2001

Group Art Unit
2661

OCT 23 2003

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	1.	U.S. 5,790,539	08-04-1998	Chao et al.			
	2.	U.S. 5,831,980	11-03-1998	Varma et al.			
	3.	U.S. 6,069,895	05-30-2000	Ayandeh			

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FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
	4.	EP 0 241 152 A2	10-14-1987	EUROPE				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

5.	NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS. SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987
6.	International Search Report PCT/CA02/00810, August 1, 2003

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.